

idé

strukturerad  
programmering

JSP

.asm

```

.org $0
jmp START

.dseg
.org $60
ARR: .byte $0C

.cseg
START:
ldi r16,HIG
out SPH,r16
ldi r16,LOW
out SPL,r16
call HW_INIT
:
:
:

```

preprocessor  
kompilator

.hex

```

:1001000049726F6E
:1001100006D616964
:100120000656E7275
:1001300006C657A20
:
:
:
:

```

JTAG

prog

programminne  
FLASH

0	1	2
3	4	5
6	7	8
9	10	11
12	13	14
15	16	17
18	19	20
21	22	23
24	25	26
27	28	29
30	31	32

br\*  
jmp  
call

out

lpm

I/O-  
register

in

out

innanför

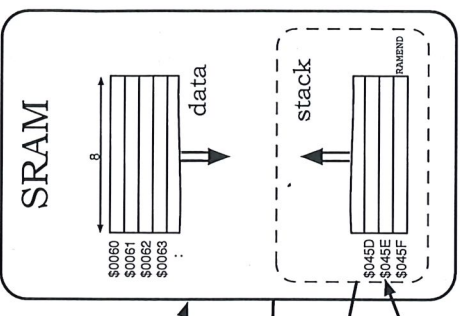
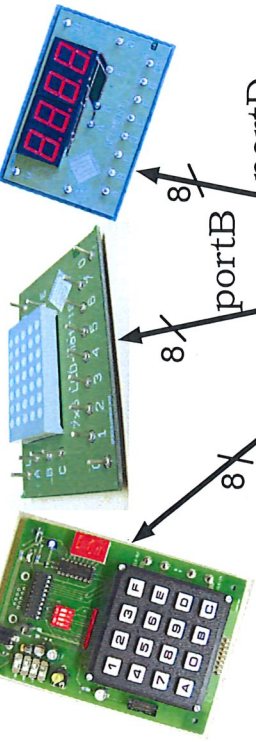
portA

portB

portD

utanför

interrupt



st

ld

push

pop

mov

add

register

8

r0

r1

r2

...

r15

mov

r16

r17

...

r30

r31

konstan-  
ter

ldi

subi

ori

: