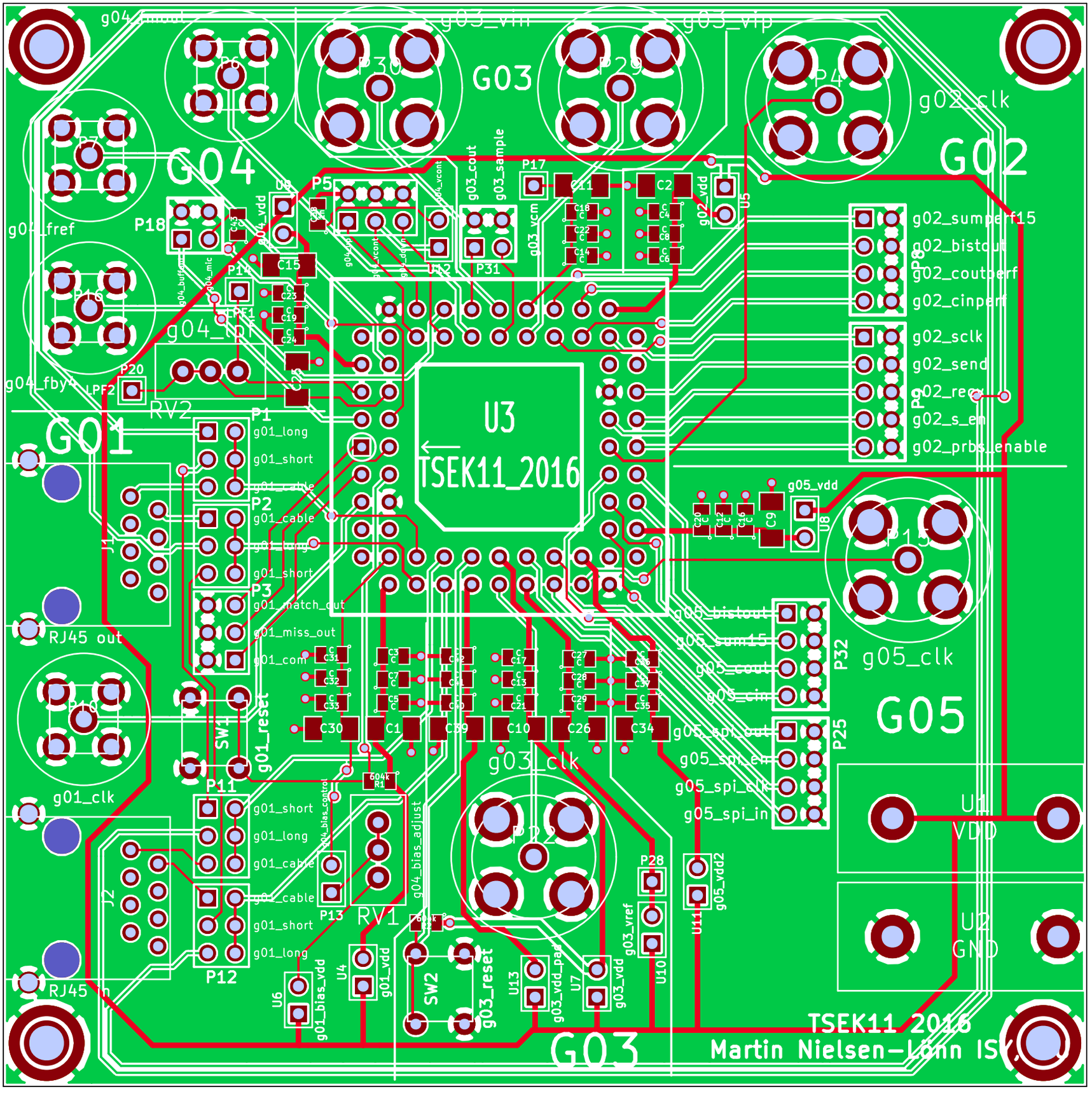
U3
TSEK11_2016
G01
G02
G03
G04
G05
g01_clk
g02_clk
g03_clk
g04_clk
g05_clk
RJ45 out
RJ45 in
g01_reset
g03_reset
g04_bias_adjust
g04_bias_control
g04_lpf
RV1
RV2
SW1
SW2
P1
P2
P3
P4
P5
P6
P7
P8
P9
P10
P11
P12
P13
P14
P15
P16
P17
P18
P20
P22
P25
P28
P29
P30
P31
P32
g01_long
g01_short
g01_cable
g01_com
g01_match_out
g01_miss_out
g02_sumperf15
g02_bistout
g02_coutperf
g02_cinperf
g02_sclk
g02_send
g02_recv
g02_s_en
g02_prbs_enable
g03_cout
g03_sample
g03_vcm
g03_vcont
g03_vdd
g03_vdd_perf
g03_vref
g03_vip
g03_vin
g04_buffcont
g04_fref
g04_fby4
g04_mic
g04_vcont
g04_vdd
g05_bistout
g05_sum15
g05_cout
g05_cin
g05_spi_out
g05_spi_en
g05_spi_clk
g05_spi_in
g05_vdd
g05_vdd2
U1
VDD
U2
GND
TSEK11_2016
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