

# TSEI03 Homework 1: Solution

For this circuit, we have  $V_{GS} = V_{in}$  and  $V_{DS} = V_{out} = V_{DD} - RI_D$ . Further,  $V_{SB} = 0 \Rightarrow V_T = V_{T0}$ , and  $\lambda = 0 \Rightarrow$  current can be written

$$V_{in} \leq V_{T0} \Rightarrow I_D = 0 \rightarrow \langle \text{cutoff} \rangle$$

$$V_{in} > V_{T0} \Rightarrow I_D = k' \frac{W}{L} V_{min} \left( V_{in} - V_{T0} - \frac{V_{min}}{2} \right),$$

$$\text{where } V_{min} = \min(V_{in} - V_{T0}, V_{DD} - RI_D, V_{DSAT}) \rightarrow \langle \text{saturated, resistive, velocity sat.} \rangle$$

## 1. $V_{in} = 0 \text{ V}$

$$V_{in} \leq V_{T0} \Rightarrow I_D = 0 \Rightarrow V_{out} = V_{DD}$$

$$\boxed{\text{MOSFET is cutoff, } I_D = 0, \text{ and } V_{out} = 2.5 \text{ V}}$$

## 2. $V_{in} = 0.5 \text{ V}$

$$V_{in} > V_{T0}, \text{ guess MOSFET is saturated} \Rightarrow V_{min} = V_{in} - V_{T0} = 0.07 \text{ V}$$

$$I_D = k' \frac{W}{L} \frac{(V_{in} - V_{T0})^2}{2} = 1.7 \mu\text{A}$$

$$V_{out} = V_{DD} - RI_D = 2.49 \text{ V}$$

$$\text{Check mode: } V_{min} = \min(0.07, 2.49, 0.63) \text{ V} \rightarrow \text{saturated is correct}$$

$$\boxed{\text{MOSFET is saturated, } I_D = 1.7 \mu\text{A}, \text{ and } V_{out} = 2.49 \text{ V}}$$

## 3. $V_{in} = 1.0 \text{ V}$

$$V_{in} > V_{T0}, \text{ guess MOSFET is saturated} \Rightarrow V_{min} = V_{in} - V_{T0} = 0.57 \text{ V}$$

$$I_D = k' \frac{W}{L} \frac{(V_{in} - V_{T0})^2}{2} = 112 \mu\text{A}$$

$$V_{out} = V_{DD} - RI_D = 1.93 \text{ V}$$

$$\text{Check mode: } V_{min} = \min(0.57, 1.93, 0.63) \text{ V} \rightarrow \text{saturated is correct}$$

$$\boxed{\text{MOSFET is saturated, } I_D = 112 \mu\text{A}, \text{ and } V_{out} = 1.93 \text{ V}}$$

## 4. $V_{in} = 1.5 \text{ V}$

$$V_{in} > V_{T0}, \text{ guess MOSFET is saturated} \Rightarrow V_{min} = V_{in} - V_{T0} = 1.07 \text{ V}$$

$$I_D = k' \frac{W}{L} \frac{(V_{in} - V_{T0})^2}{2} = 395 \mu\text{A}$$

$$V_{out} = V_{DD} - RI_D = 0.52 \text{ V}$$

$$\text{Check mode: } V_{min} = \min(1.07, 0.52, 0.63) \text{ V} \rightarrow \text{saturated is wrong}$$

$$\text{Reguess MOSFET is velocity saturated} \Rightarrow V_{min} = V_{DSAT} = 0.63 \text{ V}$$

$$I_D = k' \frac{W}{L} V_{DSAT} \left( V_{in} - V_{T0} - \frac{V_{DSAT}}{2} \right) = 328 \mu\text{A}$$

$$V_{out} = V_{DD} - RI_D = 0.86 \text{ V}$$

Check mode:  $V_{\min} = \min(1.07, 0.86, 0.63) \text{ V} \rightarrow$  velocity saturated is correct

**MOSFET is velocity saturated,  $I_D = 328 \mu\text{A}$ , and  $V_{\text{out}} = 0.86 \text{ V}$**

### 5. $V_{\text{in}} = 2.0 \text{ V}$

$V_{\text{in}} > V_{T0}$ , guess MOSFET is velocity saturated  $\Rightarrow V_{\min} = V_{\text{DSAT}} = 0.63 \text{ V} \Rightarrow$

$$I_D = k' \frac{W}{L} V_{\text{DSAT}} \left( V_{\text{in}} - V_{T0} - \frac{V_{\text{DSAT}}}{2} \right) = 546 \mu\text{A}$$

$V_{\text{out}} = V_{\text{DD}} - RI_D = -0.23 \text{ V} \rightarrow$  velocity saturated is incorrect

Reguess MOSFET is resistive  $\Rightarrow V_{\min} = V_{\text{out}} \Rightarrow$

$$Rk' \frac{W}{L} \frac{1}{2} V_{\text{out}}^2 - \left[ 1 + Rk' \frac{W}{L} (V_{\text{in}} - V_{T0}) \right] V_{\text{out}} + V_{\text{DD}} = 0 \Rightarrow$$

$$V_{\text{out}} = \begin{cases} 0.44 \text{ V} \\ (3.28 \text{ V} > V_{\text{DD}}) \end{cases} \Rightarrow I_D = 412 \mu\text{A}$$

Check mode:  $V_{\min} = \min(1.57, 0.44, 0.63) \text{ V} \rightarrow$  resistive is correct

**MOSFET is resistive,  $I_D = 412 \mu\text{A}$ , and  $V_{\text{out}} = 0.44 \text{ V}$**

### 6. $V_{\text{in}} = 2.5 \text{ V}$

$V_{\text{in}} > V_{T0}$ , guess MOSFET is resistive  $\Rightarrow V_{\min} = V_{\text{out}} \Rightarrow$

$$Rk' \frac{W}{L} \frac{1}{2} V_{\text{out}}^2 - \left[ 1 + Rk' \frac{W}{L} (V_{\text{in}} - V_{T0}) \right] V_{\text{out}} + V_{\text{DD}} = 0 \Rightarrow$$

$$V_{\text{out}} = \begin{cases} 0.33 \text{ V} \\ (4.39 \text{ V} > V_{\text{DD}}) \end{cases} \Rightarrow I_D = 434 \mu\text{A}$$

Check mode:  $V_{\min} = \min(2.07, 0.33, 0.63) \text{ V} \rightarrow$  resistive is correct

**MOSFET is resistive,  $I_D = 434 \mu\text{A}$ , and  $V_{\text{out}} = 0.33 \text{ V}$**

**Plot  $V_{\text{out}}(V_{\text{in}})$**

